

DNS - GW block diagram

VER1.0

14.7456MHz

BYTE SELECT SW 1-3

0:1OFF 2OFF 3OFF 8BYTE	1:10N 2OFF 3OFF 16BYTE	2:1OFF 2ON 3OFF 24BYTE
3:1ON 2ON 3OFF 32BYTE	4:1OFF 2OFF 3ON 40BYTE	5:1ON 2OFF 3ON 48BYTE
6:1OFF 2ON 3ON 56BYTE	7:1ON 2ON 3ON 64BYTE	

I/F SELECT SW 5-6 5:ON=TTL OFF=RS-232C 6:ON=HOST OFF=SLAVE

NONUSE SW 4

Serial Baud rate Setting

0:9600 1:19.2K 2:38.4K 3:57.6K 4:115.2K

Serial Channel Setting

00-99

CPU H8

data bus

USER RS-232C UNIT

RS-232C MAX232C

USER Serial UNIT

I/O connector

TTL 8BIT

PORT1

PORT2

PORT3

PORT4

PORT5

PORT6

PORT7

PORT8

+5V

GND

RXD

TXD

Supply power DC24V

DC-DCconverter

TO DEGITAL 1.5W +5V 0.3A

PCA82C251 RS-485 transceiver

HCPL-7720

HCPL-7720

OMRON CAN CHIP

DeviceNet

DC-DC NJM2360

+5V

DeviceNet 125K/250K/500KBPS Baud rate Setting AUTO B.RATE

DeviceNet CHANNEL SETTING 00 ~ 63

DPRAM

16MHz

