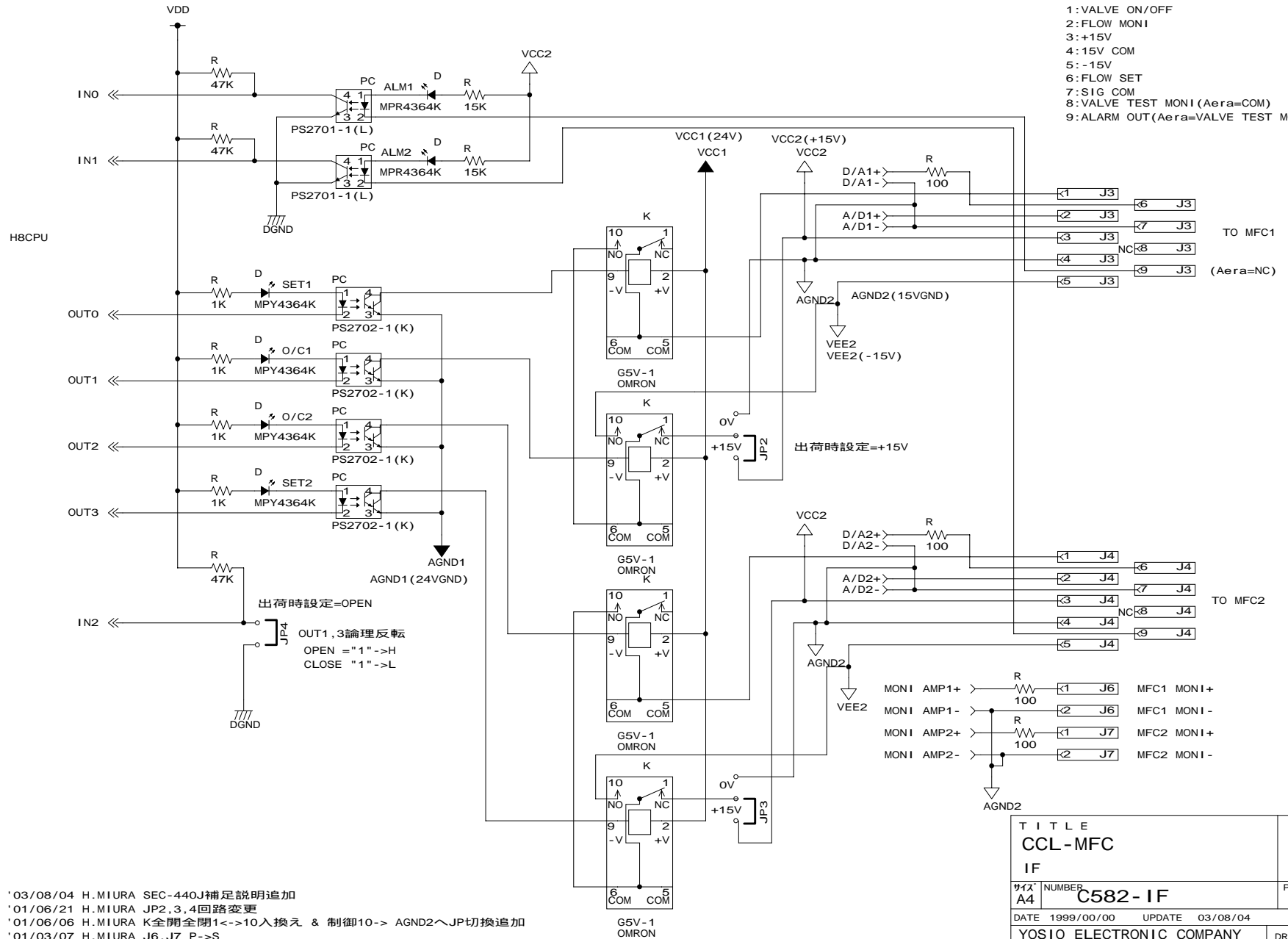


VCC1<- ISO->VDD, VCC2, VEE2
AGND1<- ISO->DGND=AGND2

SEC-440J(STEC)

- 1: VALVE ON/OFF
- 2: FLOW MONI
- 3: +15V
- 4: 15V COM
- 5: -15V
- 6: FLOW SET
- 7: SIG COM
- 8: VALVE TEST MONI (Aera=COM)
- 9: ALARM OUT (Aera=VALVE TEST MONI)



REV.05 '03/08/04 H.MIURA SEC-440J補足説明追加
REV.04 '01/06/21 H.MIURA JP2,3,4回路変更
REV.03 '01/06/06 H.MIURA K全開全閉1<->10入換え & 制御10-> AGND2へJP切換追加
REV.02 '01/03/07 H.MIURA J6, J7 P->S
REV.01 '01/02/25 H.MIURA MONI ADD, AGND2訂正

TITLE		APPROVED	
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YOSIO ELECTRONIC COMPANY		Rev. 05	
HIGASHI KURUME-SHI, TOKYO, JAPAN		DRAWN	
Tel. 0424-71-2587 Fax. 0424-71-2588		H.MIURA	

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