

CCL – GW block diagram

VER1.1

SENJU KYOKUSU SELECT SW7 1,2
 1:ON 2:ON = 1 KYOKU 1:OFF 2:ON = 2 KYOKU
 1:ON 2:OFF = 3 KYOKU 1:OFF 2:OFF = 4 KYOKU
 I/F SELECT SW7-5,6 5:ON=TTL OFF=RS-232C 6:ON=SLAVE OFF=HOST
 NONUSE SW7-3,4

CPU H8

Serial Baud rate Setting
 0:9600 1:19.2K 2:38.4K 3:57.6K 4:115.2K

Serial Channel Setting
 00-99

USER
 RS-232C UNIT

RS-232C MAX232C

USER
 Serial UNIT

I/O connector
 TTL 8BIT
 PORT1
 PORT2
 PORT3
 PORT4
 PORT5
 PORT6
 PORT7
 PORT8
 +5V
 GND
 RXD
 TXD

USER
 RS-485 OPTION BOARD

Supply power DC24V

DC-DCconverter

TO DIGITAL
 1.5W +5V 0.3A

SN75ALS181NS(T.I.)
 ZCYS51R5-M3PAT(TDK)
 HZU6.2Z(HITACHI)

CC-Link Twist Pair

RS-485 transceiver

CC-Link Baud rate Setting
 10Mbps Max

CC-Link CHANNEL SETTING
 01 ~ 64

MITSUBISHI LSI MFP3

A6GA-CCMFP3N60F

data bus 10MHz

DSO751SB-80MHz

JUMPER